

## Objective

The Department of Electronics Engineering, Aligarh Muslim University, Aligarh is organising a workshop on Recent Trends in VLSI Design and Technology (VDT-2010) on 20<sup>th</sup> March, 2010. The workshop is expected to provide a platform to engineers, teachers, researchers and students to know about cutting edge challenges and issues in a broad area of VLSI Design and Technology. The theme of the workshop has been chosen in view of the revolution and rapid developments in the field of VLSI Design and Technology. There is a huge gap between the supply and demand of good quality VLSI Designers and Technologists in our country. This workshop is aimed at exposing the participants to the latest trends in this coveted area. All lectures in this workshop will be delivered by eminent experts from the Indian Institute of Technology and other reputed organisations with proven track record.

## The Department

The Department of Electronics Engineering is considered to be one of the most active departments of the University in terms of publications, R & D output and sponsored research projects. It is the first Department in the Faculty of Engineering to be accorded the prestigious DRS status by the University Grants Commission in 2005. The Department has been active in VLSI Design area since its inception especially in the fields of Analog Signal Processing and ASIC/FPGA based Digital System Design. Many Ph.D. and M.Tech. students are working on Challenging VLSI Design issues at the nanoscale. The Department has contributed more than 400 research papers in reputed Journals and Conferences in the area of VLSI Design, Technology and related fields.

## The University

Aligarh Muslim University (AMU) has a long and distinguished history. It started as a Mohammedan Anglo Oriental College (MAO) in 1875. It was later elevated to the status of a Central University in 1920 by an Act of Indian Legislative Council. The University has twelve faculties and 94 Departments with around 25000 students. It has set high ideals for itself, and achieved them all with remarkable tenacity of purpose and determination. Several heads of state and governments, Union Ministers, Diplomats and judges of the highest courts of a few countries have studied and obtained their degrees at AMU. Vice Chancellors of more than 50 universities, and a distinguished array of administrators and technocrats have also been products of AMU.

## Areas covered

- Nanoelectronics
- Embedded System Design
- Current mode signal processing
- Design issues in deep submicron

## Registration Fee

Category	Advance Registration (INR)	Spot Registration (INR)
Students	500	1000
Academicians	1000	2000
Industry	1500	3000

**Advance registration deadline: 15<sup>th</sup> of March 2010. Only limited seats are available.**

## Registration Form (VDT-2010)

1. Name:
2. Designation:
3. Organisation:
4. Address:
5. Phone no:
6. Fax no.:
7. Email:
8. Whether student (If yes, please attach a copy of your student ID card) Yes/No
9. Whether accommodation required (Y/N):
10. Details of Registration Fee:

Demand Draft No. \_\_\_\_\_ Dated: \_\_\_\_\_  
for Rs \_\_\_\_\_ payable at Aligarh in favour  
of "Coordinator, VDT-2010" is enclosed herewith.

Date:

Signature

**Note: Make additional copies, if required.**

## Accommodation

Limited accommodation is available in the University guest houses, hotels on first-come first-serve basis on request and payment. Participants are advised to send their requests well in advance.

## About Aligarh

Aligarh is located on the main Delhi-Howrah rail route at a distance of 135 km southeast of Delhi. The city also falls on the Grand Trunk Road and is located 292km from Kanpur. Moreover, Aligarh is 82 km from Agra and 60 km from Mathura by road. The university is about 3km from the Aligarh Railway Station.

## Address for Correspondence

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## Chief Patron

**Prof. P. K. Abdul Azis**, *Hon'ble  
Vice Chancellor, AMU*

## Patrons

**Prof. S.M.A. Rizvi**, *Dean, Faculty of  
Engineering & Technology*

**Prof. M. T. Ahmad**, *Principal, ZHCET.*

**Prof. M. A. Siddiqui**, *Chairman, Dept. of  
Electronics Engineering*

## Organising Committee

Prof. Wasim Ahmad  
Prof. Mohd. Jamilur Rahman Khan  
Prof. Pervez Mustajab,  
Prof. Mohd. Salim Beg  
Prof. Ekram Khan  
Mr. Masuir Raza Abidi  
Dr. Syed Atiqur Rahman  
Dr. Omar Farooq  
Dr. Mohd. Shah Alam  
Mr. Athar Ali Moinuddin  
Mr. Syed Javed Arif  
Dr. Anwar Sadat  
Mr. Mohd. Samar Ansari  
Ms. Parveen Beg  
Mr. Ale Imran

## Coordinator

**Prof. M. Hasan**, *Dept. of Electronics Engineering,*

## Co-Coordinator

**Dr. T. Parveen**, *Dept. of Electronics Engineering*  
**Dr. M.J. Siddiqui**, *Dept. of Electronics Engineering*  
**Dr. S. Maheshwari**, *Dept. of Electronics Engineering*



## WORKSHOP ON RECENT TRENDS IN VLSI DESIGN AND TECHNOLOGY

(VDT-2010)  
20<sup>th</sup> March, 2010



Organised by

Department of Electronics Engineering,  
Zakir Hussain College of Engineering and  
Technology, AMU, Aligarh.

Website: <http://www.zhcet.ac.in/vdt10>