

VLSI System Design

1st SEMESTER

Date	Day	Timing	Paper Code	Subject
1	2	3	4	5
24-01-2012	Tuesday	10:00 AM to 01:00 PM	VD101	Advance Computer Architecture
25-01-2012	Wednesday	GAP		
26-01-2012	Thursday	GAP		
27-01-2012	Friday	10:00 AM to 01:00 PM	VD102	VLSI Technology and Design
28-01-2012	Saturday	GAP		
29-01-2012	Sunday	GAP		
30-01-2012	Monday	10:00 AM to 01:00 PM	VD103	Digital Design using VHDL
31-01-2012	Tuesday	GAP		
01-02-2012	Wednesday	10:00 AM to 01:00 PM	Elective-I	
			VD012	Data Structure & Algorithms
			VD013	Modeling of Microelectronic Devices
			VD014	Solid State Electronics Devices


Controller of Examination

VLSI System Design

3rd SEMESTER

Date	Day	Timing	Paper Code	Subject
1	2	3	4	5
24-01-2012	Tuesday	10:00 AM to 01:00 PM	VD301	VLSI Test & Testability
25-01-2012	Wednesday	GAP		
26-01-2012	Thursday	GAP		
27-01-2012	Friday	10:00 AM to 01:00 PM	ELECTIVE-III	
			VD031	CMOS-RF Design
			VD034	Nano Technology


Controller of Examination

